**Assignment No. 1**

**Digital Logic Design**

**Deadline: Monday 11th February 2019 (in the start of the class)**

**All Sections**

**Solve the following Problems from the textbook ‘Digital Design’ 5th Edition (also uploaded on slate).**

1.1, 1.2, 1.4, 1.7, 1.8, 1.9, 1.10, 1.13, 1.14, 1.18, 1.20, 1.22, 1.23, 1.25, 1.27, 1.28, 1.29, 1.30, 1.31 and 1.32

2.1, 2.2, 2.3, 2.13, 2.20, 2.21, 2.22